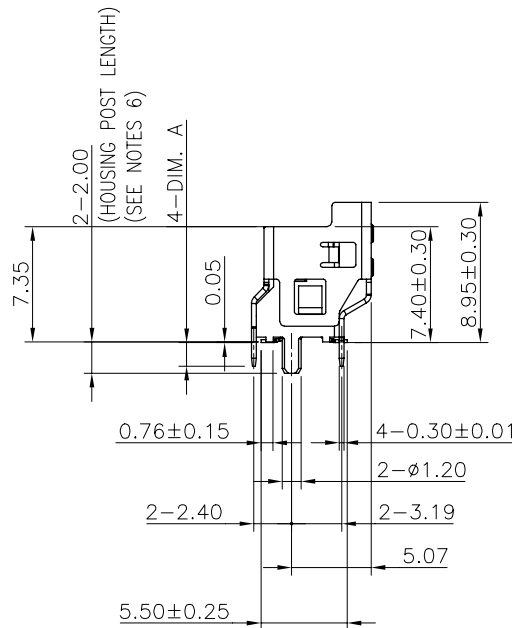
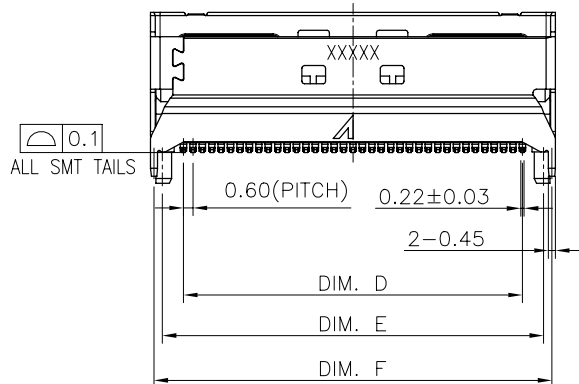
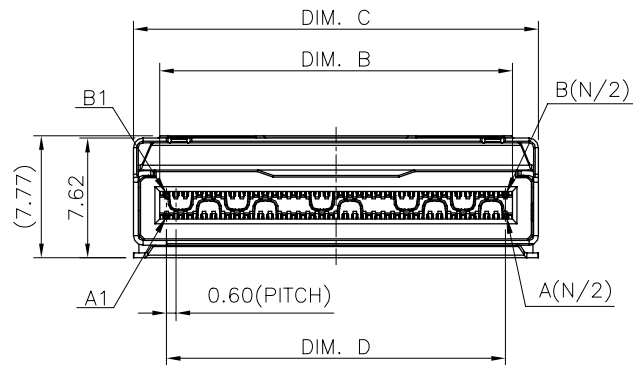
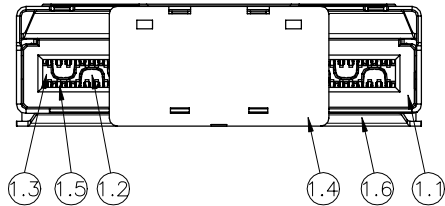
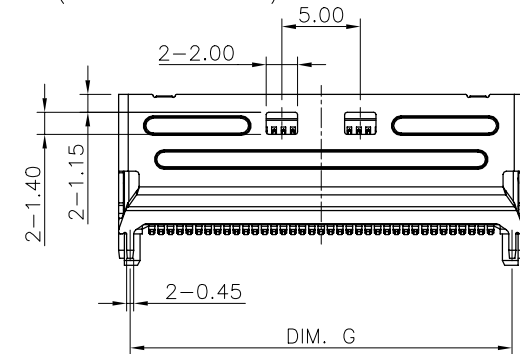




52729-XXX X X-0 X X	SHELL PIN LENGTH OPTION(DIM. A)
PACKING	1: 1.50mm
0: TAPE & REEL W/O COVER	2: 2.20mm
7: TAPE & REEL WITH COVER	3: 3.00mm
PLATING	4: 4.20mm
C: 15u" GOLD PLATING	5: 1.20mm(SHELL PIN AND HOUSING POST)
D: 30u" GOLD PLATING	6: 1.85mm
CKTS	7: 1.50mm(HOUSING POST 1.20mm)
038: 38P(4X+SIDE BAND OR 6X)	CONNECTOR SPEED
074: 74P(8X+SIDE BAND OR 12X)	1: PCIE GEN 4
050: 50P(6X+SIDE BAND OR 8X)	2: PCIE GEN 5
124: 124P(16X+SIDE BAND OR 20X)	3: PCIE GEN 5 920HM
148: 148P(20X+SIDE BAND OR 24X)	



PART NUMBER	CKTS (N)	DIM. B	DIM. C	DIM. D	DIM. E	DIM. F	DIM. G
52729-038XX-XXX	38	11.68	15.00	10.80	13.50	14.55	13.55
52729-050XX-XXX	50	15.28	18.60	14.40	17.10	18.15	17.15
52729-074XX-XXX	74	22.48	25.80	21.60	24.30	25.35	24.35

NOTES :

1. MATERIAL :

- 1.1 HOUSING : THERMOPLASTIC(LCP), HIGH TEMP., UL94V-0; COLOR : BLACK.
- 1.2 I/M : THERMOPLASTIC(LCP), HIGH TEMP., UL94V-0; COLOR : BLACK.
- 1.3 BASE : THERMOPLASTIC(LCP), HIGH TEMP., UL94V-0; COLOR : BLACK.
- 1.4 COVER : THERMOPLASTIC(LCP), HIGH TEMP., UL94V-0; COLOR : BLACK.
- 1.5 CONTACT : COPPER ALLOY
- 1.6 SHELL : STAINLESS

2. FINISH :

- 2.1 CONTACT : SEE ORDER INFORMATION.
SOLDER : MATTE TIN PLATING 100u"MIN.
UNDERPLATING : 50"MIN. NICKEL OVERALL.
- 2.2 SHELL : 50u" MIN. NICKEL UNDERPLATED OVERALL.

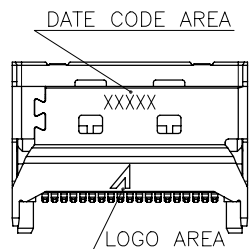
3. REFLOW SOLDER CAPABLE TO 260°C

PER ACES SPEC.

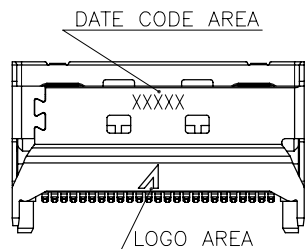
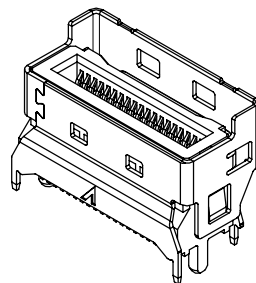
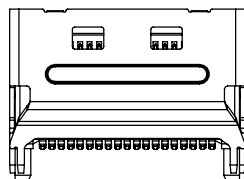
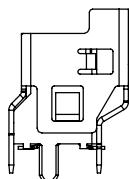
4. SPEC. PLS. REFER TO PS-52729-XXXXX-XXX

5. PART NUMBER :

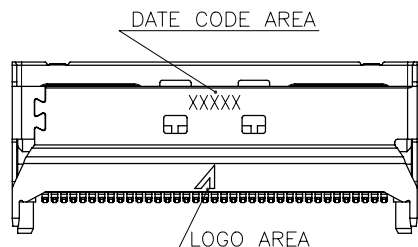
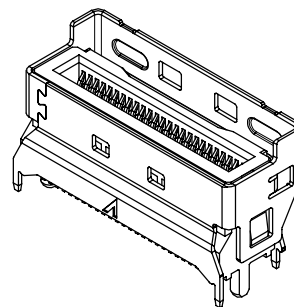
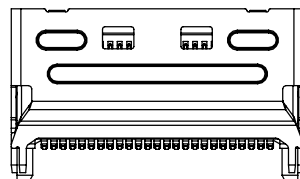
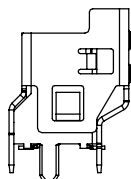
QUALITY SYMBOLS	DRAWN BY	DATE	ACES ELECTRONICS	
MAJOR	Huang, Kuo Hua	22'/03/11		
CRITICAL	CHECKED BY	DATE	MINI COOL EDGE IO INTEGRATED CONNECTOR VERTICAL, 0.6 PITCH FOR PCIE, SMT TYPE	
GENERAL TOLERANCES (UNLESS SPECIFIED)	Chen, Chun Yuan	22'/03/11		
X. ±0.5	APPROVED BY	DATE	UNITS	SIZE
.X ±0.25	Wang, Chun Sheng	22'/03/11	mm	A4
.XX ±0.15			SCALE	SHEET NO.
.XXX ±0.1			2 : 1	1 OF 7
ANGLES ±2°			REV	DWG NO.
			A	52729-XXXXX-XXX



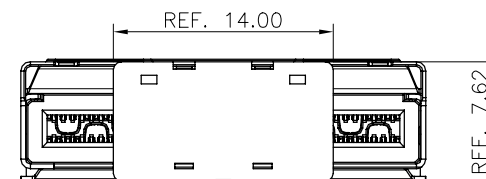
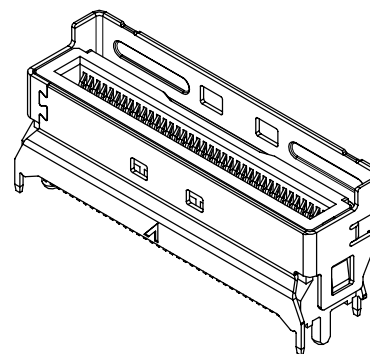
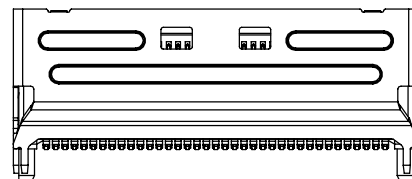
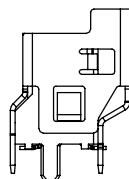
52729-038XX-XXX



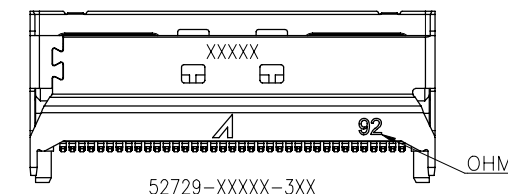
52729-050XX-XXX



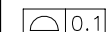
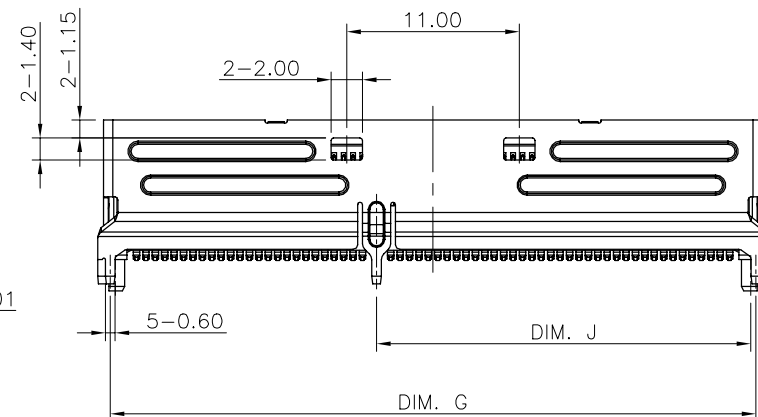
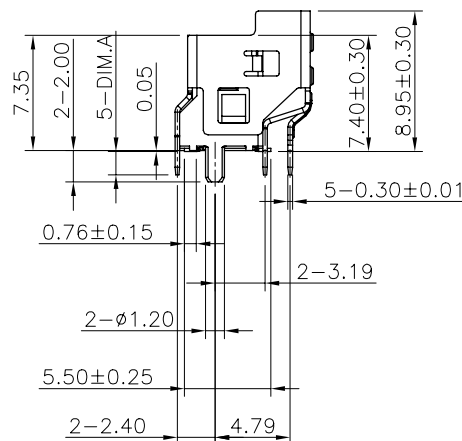
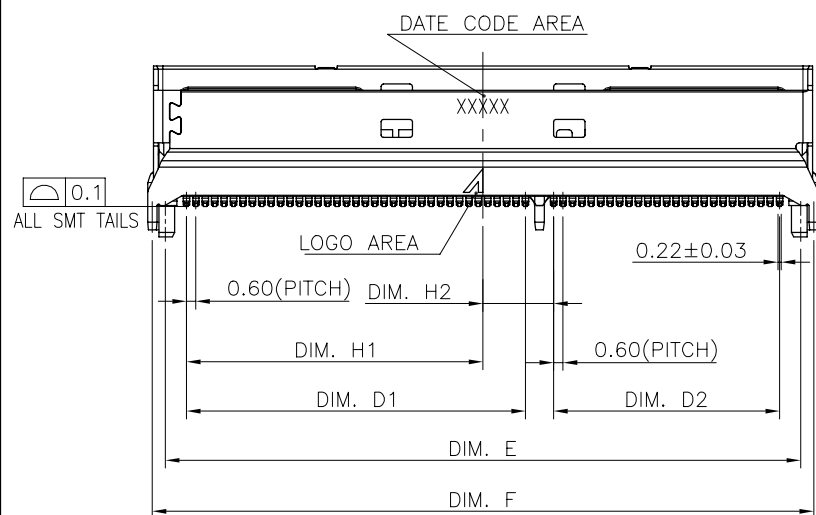
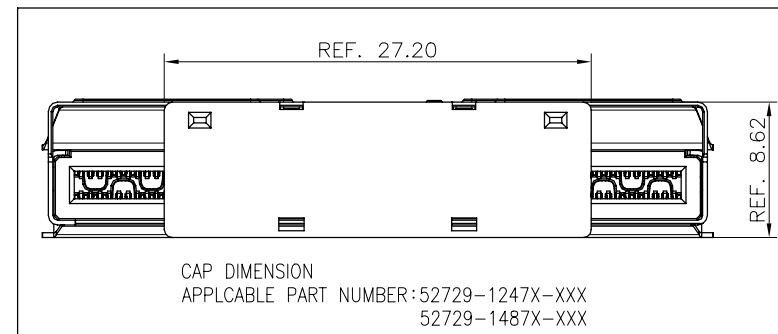
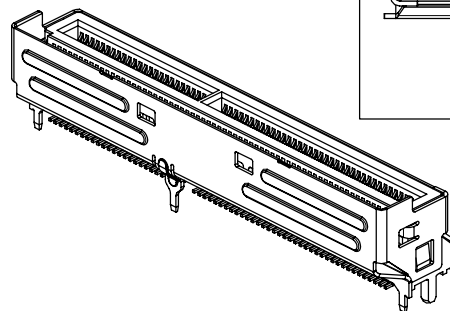
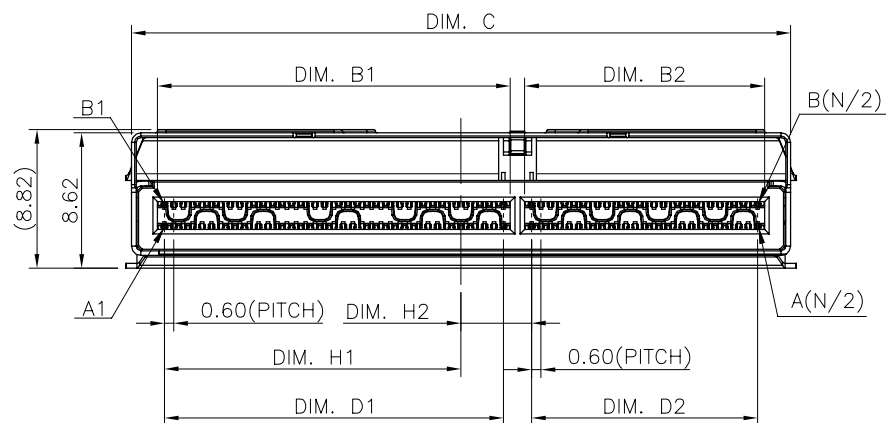
52729-074XX-XXX



CAP DIMENSION
APPLCABLE PART NUMBER: 52729-0387X-XXX
52729-0507X-XXX
52729-0747X-XXX



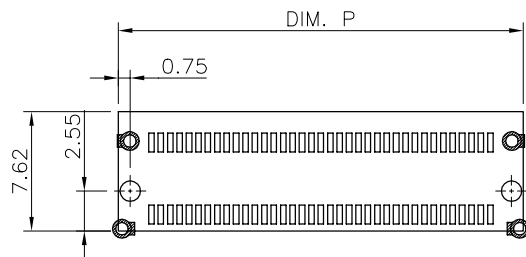
QUALITY SYMBOLS MAJOR  CRITICAL 	DRAWN BY Huang, Kuo Hua		DATE 22'/03/11					
	CHECKED BY Chen, Chun Yuan		DATE 22'/03/11					
GENERAL TOLERANCES (UNLESS SPECIFIED) X. ±0.5 .X ±0.25 .XX ±0.15 .XXX ±0.1 ANGLES ±2°	APPROVED BY Wang, Chun Sheng		DATE 22'/03/11		TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR VERTICAL, 0.6 PITCH FOR PCIE, SMT TYPE			
	UNITS mm						SIZE A4	
	SCALE 2 : 1		SHEET NO. 2 OF 7				RFQ NO. 2010058	
			REV A				DWG NO. 52729-XXXXX-XXX	



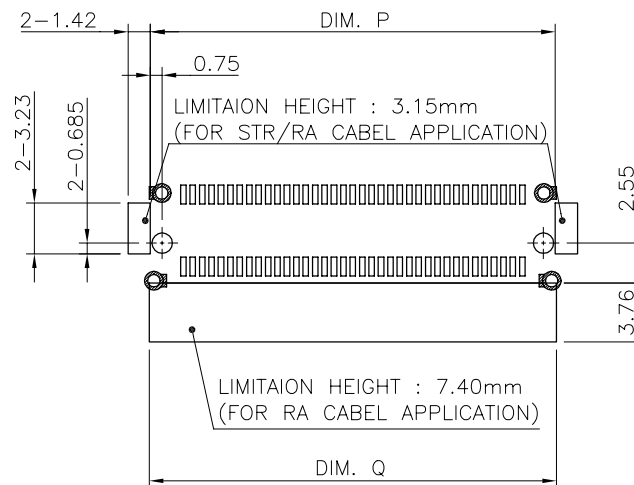
ALL SMT TAILS

PART NUMBER	CKTS (N)	DIM. B1	DIM. B2	DIM. C	DIM. D1	DIM. D2	DIM. E	DIM. F	DIM. G	DIM. H1	DIM. H2	DIM. J
52729-124XX-XXX	124	22.48	15.28	42.00	21.60	14.40	40.50	42.15	41.15	18.90	4.50	23.85
52729-148XX-XXX	148	22.48	22.48	49.20	21.60	21.60	47.70	49.35	48.35	22.50	0.90	23.85

QUALITY SYMBOLS	DRAWN BY	DATE	ACES ELECTRONICS
MAJOR	Huang, Kuo Hua	22'/03/11	
CRITICAL	CHECKED BY	DATE	
GENERAL TOLERANCES (UNLESS SPECIFIED)	Chen, Chun Yuan	22'/03/11	
X. ±0.5	APPROVED BY	DATE	TITLE
.X ±0.25	Wang, Chun Sheng	22'/03/11	MINI COOL EDGE IO INTEGRATED CONNECTOR VERTICAL, 0.6 PITCH FOR PCIE, SMT TYPE
.XX ±0.15	UNITS	mm	SIZE
.XXX ±0.1	SCALE	2 : 1	A4
ANGLES ±2°	SHEET NO.	3 OF 7	REV
	DWG NO.	52729-XXXXX-XXX	2010058

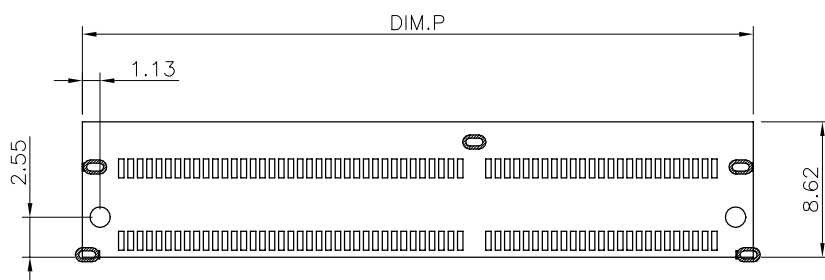


COMPONENTS KEEP OUT AREA
LIMITATION HEIGHT: 0mm

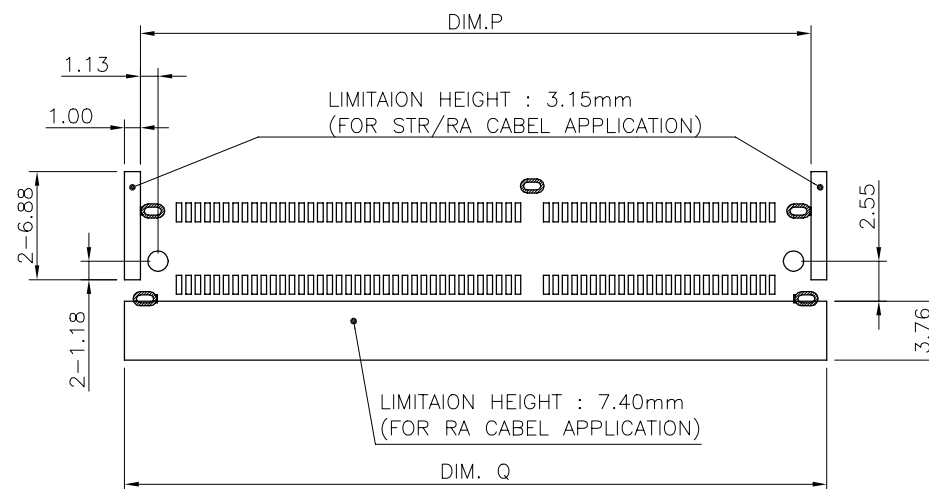


COMPONENTS KEEP OUT AREA

PART NUMBER	CKTS (N)	DIM. P	DIM. Q
52729-038XX-XXX	38	15.00	15.15
52729-050XX-XXX	50	18.60	18.75
52729-074XX-XXX	74	25.80	25.95



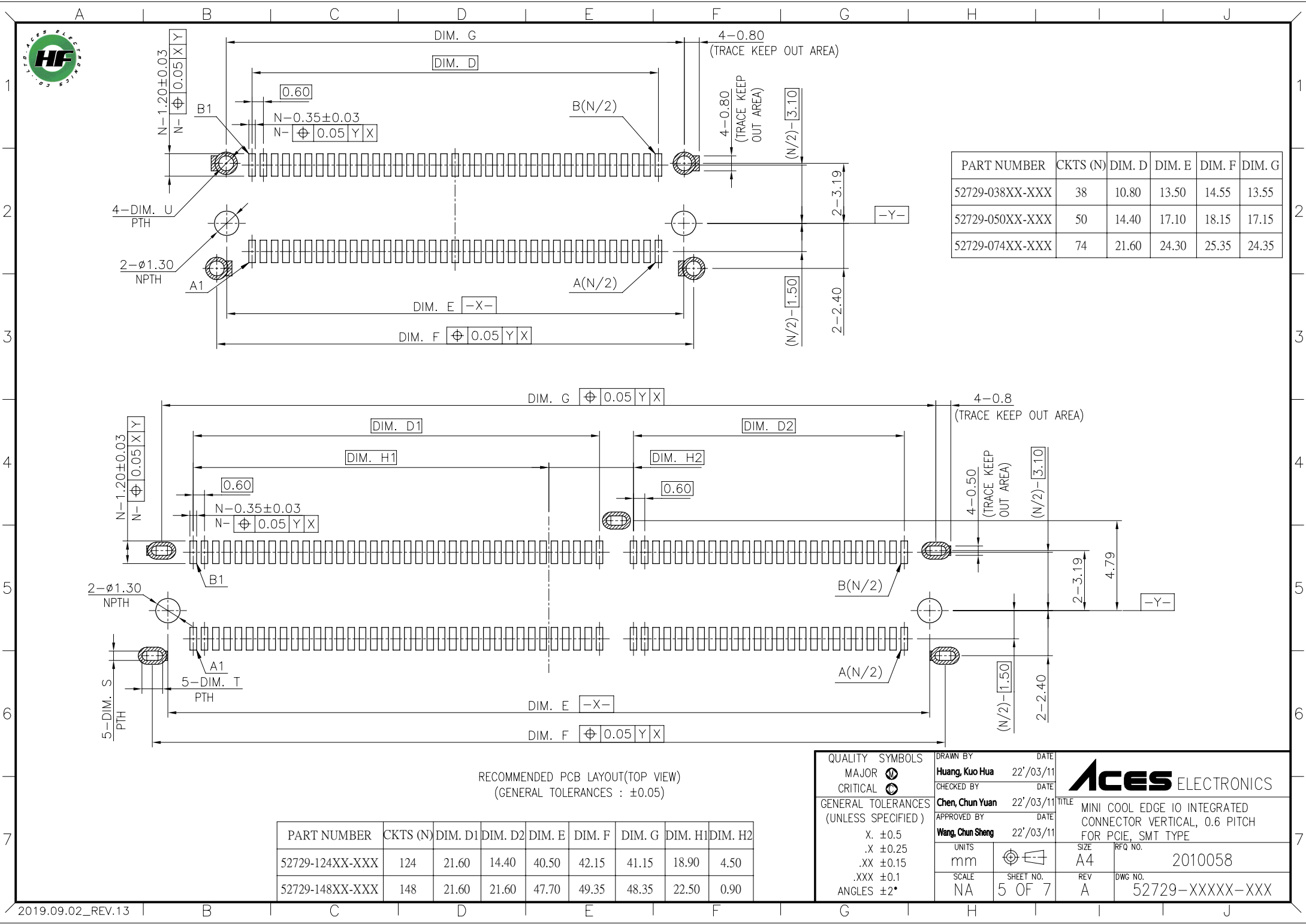
COMPONENTS KEEP OUT AREA
LIMITATION HEIGHT: 0mm



COMPONENTS KEEP OUT AREA

PART NUMBER	CKTS (N)	DIM. P	DIM. Q
52729-124XX-XXX	124	42.75	44.75
52729-148XX-XXX	148	49.95	51.95

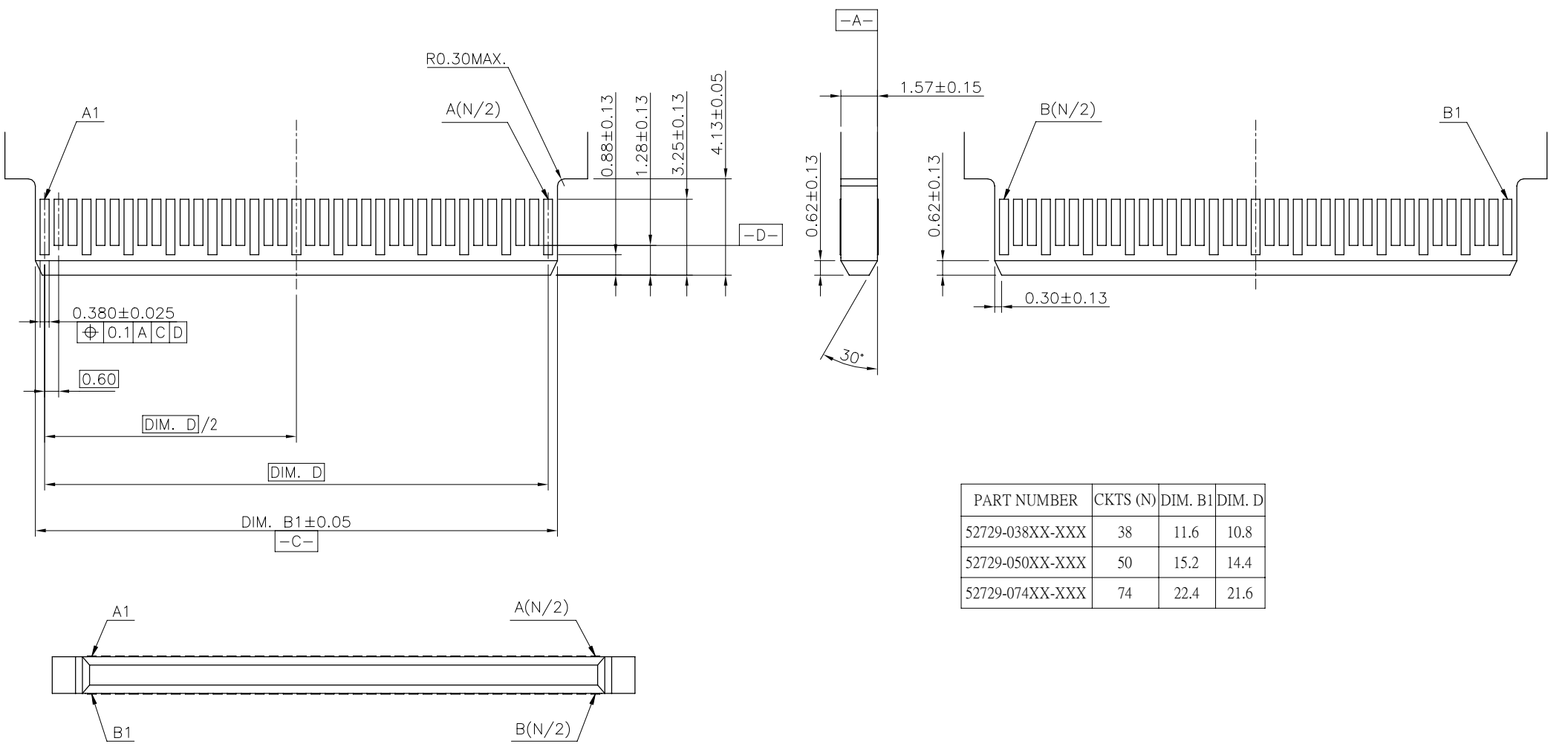
QUALITY SYMBOLS MAJOR CRITICAL GENERAL TOLERANCES (UNLESS SPECIFIED) X. ±0.5 .X ±0.25 .XX ±0.15 .XXX ±0.1 ANGLES ±2°	DRAWN BY Huang, Kuo Hua	DATE 22'/03/11		
	CHECKED BY Chen, Chun Yuan	DATE 22'/03/11		
	APPROVED BY Wang, Chun Sheng	DATE 22'/03/11	TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR VERTICAL, 0.6 PITCH FOR PCIE, SMT TYPE	
	UNITS mm		SIZE A4	RFQ NO. 2010058
SCALE NA	SHEET NO. 4 OF 7	REV A	DWG NO. 52729-XXXXX-XXX	



PART NUMBER	CKTS (N)	DIM. D1	DIM. D2	DIM. E	DIM. F	DIM. G	DIM. H1	DIM. H2
52729-124XX-XXX	124	21.60	14.40	40.50	42.15	41.15	18.90	4.50
52729-148XX-XXX	148	21.60	21.60	47.70	49.35	48.35	22.50	0.90

SIZE A4	RFQ NO. 2010058
REV A	DWG NO. 52729-XXXXX-XXX

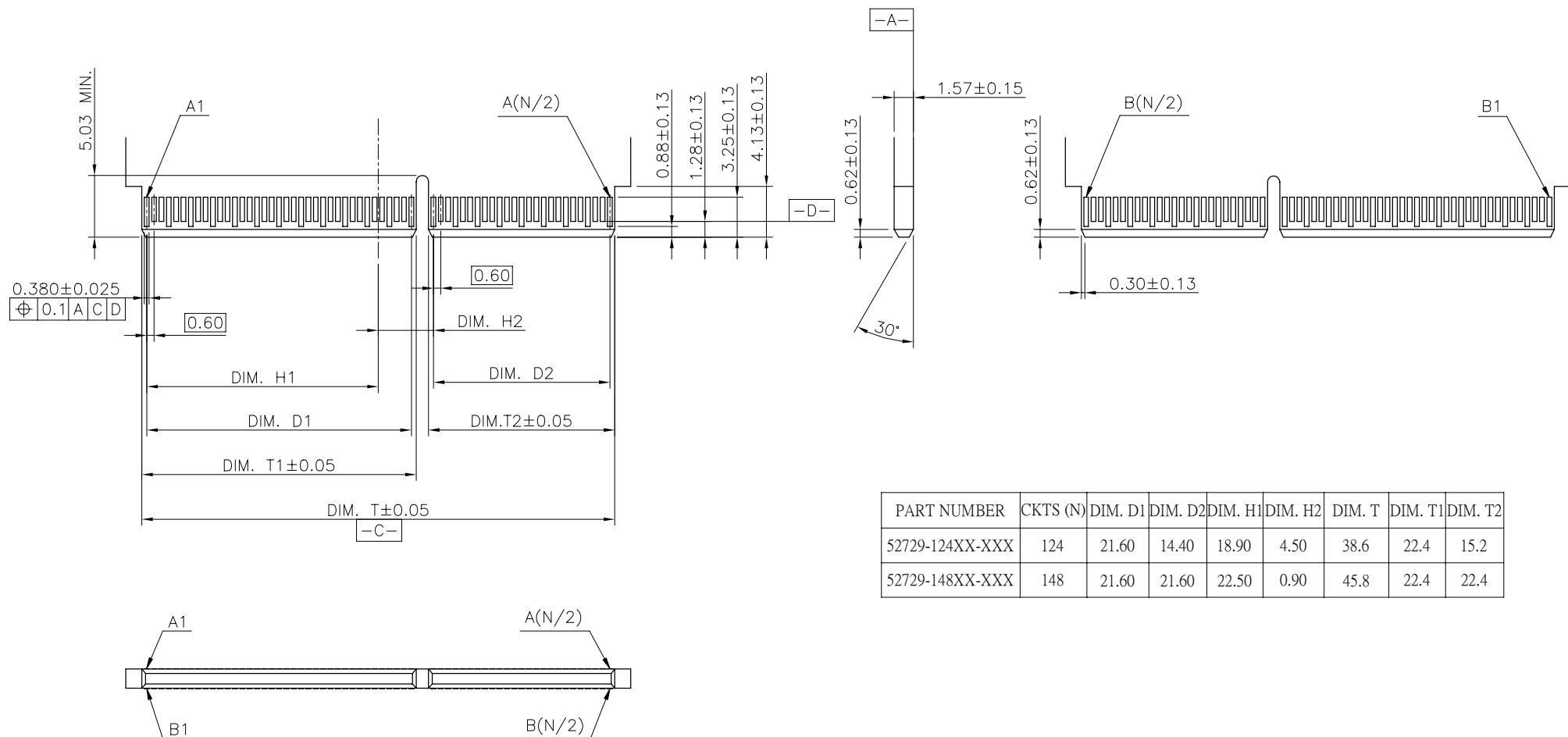
2019.09.02_REV.13



PART NUMBER	CKTS (N)	DIM. B1	DIM. D
52729-038XX-XXX	38	11.6	10.8
52729-050XX-XXX	50	15.2	14.4
52729-074XX-XXX	74	22.4	21.6

RECOMMENDED AIC MATING CARD DIMENSIONS(1 PORT)
GENERAL TOLERANCES : ±0.05

QUALITY SYMBOLS MAJOR CRITICAL	 	DRAWN BY	DATE		
		Huang, Kuo Hua	22'/03/11		
GENERAL TOLERANCES (UNLESS SPECIFIED)		CHECKED BY	DATE		
		Chen, Chun Yuan	22'/03/11		
		TITLE			
		APPROVED BY	DATE	MINI COOL EDGE IO INTEGRATED CONNECTOR VERTICAL, 0.6 PITCH FOR PCIE, SMT TYPE	
		Wang, Chun Sheng	22'/03/11		
X. ±0.5 .X ±0.25 .XX ±0.15 .XXX ±0.1 ANGLES ±2°		UNITS mm		SIZE A4	RFQ NO. 2010058
		SCALE NA	SHEET NO. 6 OF 7	REV A	DWG NO. 52729-XXXXX-XXX



RECOMMENDED AIC MATING CARD DIMENSIONS(2 PORT_124 &148 PIN)
GENERAL TOLERANCES : ±0.05

QUALITY SYMBOLS MAJOR CRITICAL GENERAL TOLERANCES (UNLESS SPECIFIED) X. ±0.5 .X ±0.25 .XX ±0.15 .XXX ±0.1 ANGLES ±2°	DRAWN BY Huang, Kuo Hua	DATE 22'/03/11		
	CHECKED BY Chen, Chun Yuan	DATE 22'/03/11		
	APPROVED BY Wang, Chun Sheng	DATE 22'/03/11	TITLE MINI COOL EDGE IO INTEGRATED CONNECTOR VERTICAL, 0.6 PITCH FOR PCIe, SMT TYPE	
	UNITS mm	SCALE NA	SIZE A4	RFQ NO. 2010058
SHEET NO. 7 OF 7		REV A	DWG NO. 52729-XXXXX-XXX	